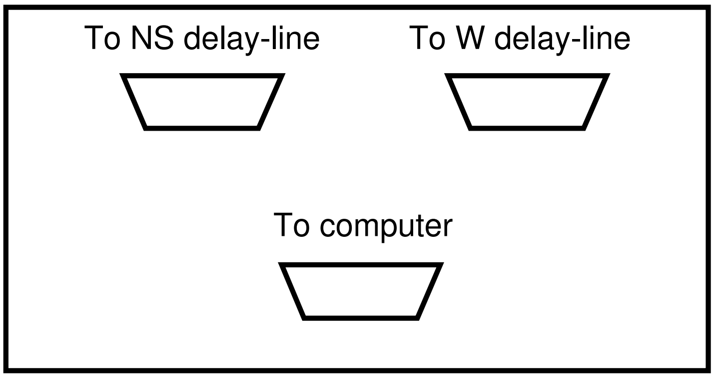
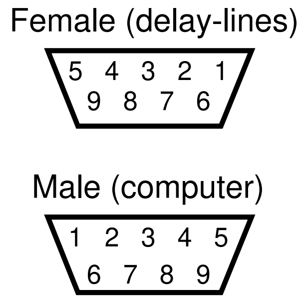
Long delay-line limit interface box

by Sébastien Morel,October 1999

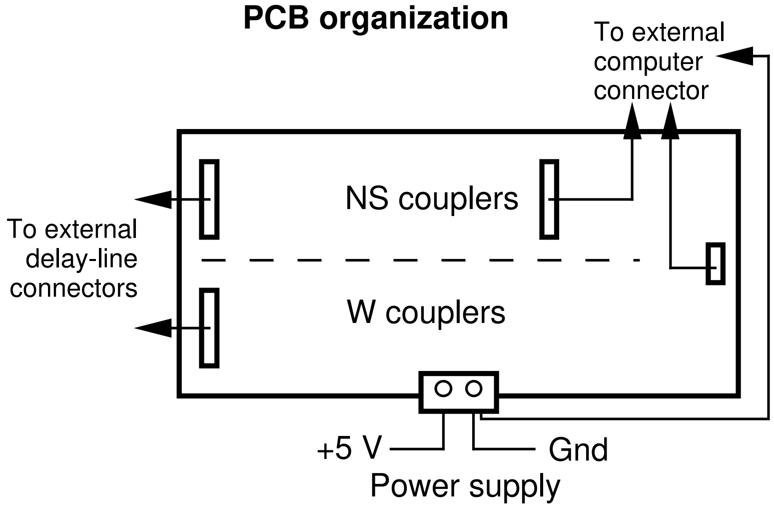
External view



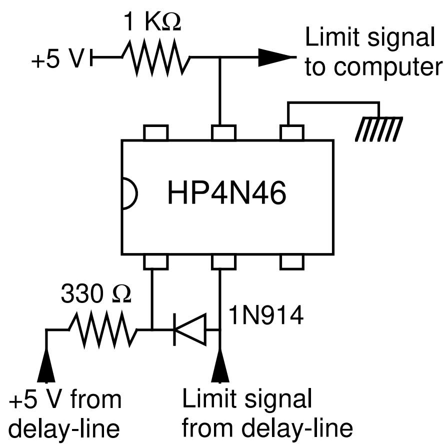
Sub-D connectors



PCB organization



Coupler



(design by Ch. Coldwell)

Connector pinout

Pin #	To NS delay-line	To W delay-line	To computer
1	Home (in)	Home (in)	Lim + W (out)
2	Lim + (in)	Lim + (in)	Home W (out)
3	Lim - (in)	Lim - (in)	Lim + NS (out)
4	n.c.	n.c.	Home NS (out)
5	n.c.	n.c.	Lim - NS (out)
6	Motor off (out)	Motor off (out)	Lim - W (out)
7	n.c.	n.c.	Motor off NS (in)
8	n.c.	n.c.	Motor off W (in)
9	+5 V tank (in)	+5 V tank (in)	Gnd